ABSTRACT:

The invention relates to a packet switching arrangement comprising a switching network of a plurality of bufferless switching matrices (6) and a plurality of cascade switch controls (7) assigned to one switching matrix (6) each, which switch controls (7) respectively include

- an identification analyzer (12) for identifying the input port in a route identification assigned to a packet,
- an output allocator (13) for evaluating the route identification,
- a configuration unit (14) for storing accepted assignments of a respective input port to an output port,
- an identification assignment analyzer (15) for changing and guiding the route identification to a port control (2 to 5).